

Birla Vishvakarma Mahavidyalaya (An Autonomous Institution)
Electronics and Communication Engineering Department
Programme Structure for
Honours in VLSI Design and Testing

Can be Opted by: Students of B.Tech. (Electronics and Communication Engineering) as per [AR for Minors / Honours](#)

Sr. No.	Course Code	Name of Course	L	T	P	H	C
1	HVD01	Advanced EDA Tool for VLSI Design	3	0	2	5	4
2		Programme Elective I	3	0	2	5	4
3		Programme Elective II	3	0	2	5	4
4		Programme Elective III	3	0	2	5	4
5	HVD91	Project	0	0	8	8	4
Total			12	0	16	28	20

Pool of Courses for Programme Elective - I, II, III

Sr. No.	Course Code	Name of Course	L	T	P	H	C
1	HVD11	Digital VLSI Testing	3	0	2	5	4
2	HVD12	Analog VLSI Design	3	0	2	5	4
3	HVD13	VLSI Design Flow: RTL to GDS	3	0	2	5	4
4	HVD14	Low Power VLSI Circuits & Systems	3	0	2	5	4
5	HVD15	VLSI Design Verification and Test	3	0	2	5	4
6	HVD16	VLSI Physical Design	3	0	2	5	4
7	HVD17	Optimization Techniques for Digital VLSI Design	3	0	2	5	4
8	HVD18	C-Based VLSI Design	3	0	2	5	4